

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4	((margin OR extreme) ADJ test\$3) AND computer AND (BMC OR (baseboard ADJ management ADJ controller)) AND frequenc\$3 AND ((digital ADJ parameter) WITH (frequency ADJ synthesizer)) AND (communicat\$4 OR transmit\$4 OR receiv \$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56
L2	4	((margin OR extreme) ADJ test\$3) AND computer AND (BMC OR (baseboard ADJ management ADJ controller)) AND frequenc\$3 AND ((digital ADJ parameter) AND (frequency ADJ synthesizer)) AND (communicat\$4 OR transmit\$4 OR receiv \$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56
L3	4	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND ((digital ADJ parameter) AND (frequency ADJ synthesizer)) AND (communicat\$4 OR transmit\$4 OR receiv \$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56

L4	6	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND ((digital ADJ parameter)) AND (communicat\$4 OR transmit\$4 OR receiv \$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56
L5	294	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND (communicat\$4 OR transmit\$4 OR receiv \$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56
L6	24	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND communicat\$4 AND transmit\$4 AND receiv \$3 AND diagnos\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56
L7	118	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND communicat\$4 AND transmit\$4 AND receiv \$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56
L8	12	((frequenc\$3 ADJ (margin OR extrem \$3)) WITH test\$3) AND server	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56
L9	57	((frequenc\$3 ADJ (margin OR extrem \$3)) WITH test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56
L10	43	((integrated ADJ circuit) OR IC) AND amplif\$3 AND switch \$3 AND (analog ADJ multiplex\$3) AND (serial ADJ communications ADJ interface) AND operation\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56

L11	1	((integrated ADJ circuit) OR IC) AND amplif\$3 AND (operational ADJ characteristic) AND switch AND (analog ADJ multiplex\$3) AND (serial ADJ communications ADJ interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56
L12	43	((integrated ADJ circuit) OR IC) AND amplif\$3 AND switch \$3 AND (analog ADJ multiplex\$3) AND (serial ADJ communications ADJ interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56
L13	42	((integrated ADJ circuit) OR IC) AND amplif\$3 AND switch AND (analog ADJ multiplex\$3) AND (serial ADJ communications ADJ interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:56
L14	4	(expander WITH generat\$3 WITH bit WITH pattern) SAME control\$4	US-PGPUB; USPAT; DERWENT	OR	ON	2008/06/04 16:56
L15	0	(expander WITH generat\$3 WITH bit WITH pattern) SAME control\$4 SAME synthesizer	US-PGPUB; USPAT; DERWENT	OR	ON	2008/06/04 16:56
L16	205	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND (communicat\$4 OR transmit\$4 OR receiv \$3).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:58
L17	297	((margin OR extreme) ADJ test\$3) AND control\$4 AND frequenc\$3 AND (communicat\$4 OR transmit\$4 OR receiv \$3).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/04 16:59

L18	1140	((702/106) or (702/117) or (714/724) or (324/537)).OCLS.	US-PGPUB	OR	OFF	2008/06/04 17:38
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